


FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT		Atty Docket SUND 260		Application No. 10/044,950			
		Applicant Chuan-Cheng HSAIO et al.					
		Filing Date January 15, 2002		Group Unit 2871			
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA			/	108800 MAIL ROOM	MAR 21 2003	RECEIVED
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Translation
Ch	AI	JP-11-136130	5/21/1999	JAPAN			Abstract
Ch	AJ	EP 1 014 334 A2	6/28/2000	EUROPEAN			NO
Ch	AK	EP 1 014 334 A3	10/2/2000	EUROPEAN			NO
	AL			/			
	AM						
	AN						
	AO						
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
Ch	AP	"Optimized Device and Decoder for an 8-Bit Resistive DAC"; IBM Technical Disclosure Bulletin, IBM Corp., Vol. 31 No. 4, September 1988; Pages 92-94.					
Ch	AQ	"A 6-Bit Digital Data Driver for Color TFT-LCDs"; NEC Research and Development, SAITO et al., Vol. 36 No. 4, October 1995; Pages 485-492.					
	AR	/					
Examiner 2507					Date Considered 5-27-04		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							